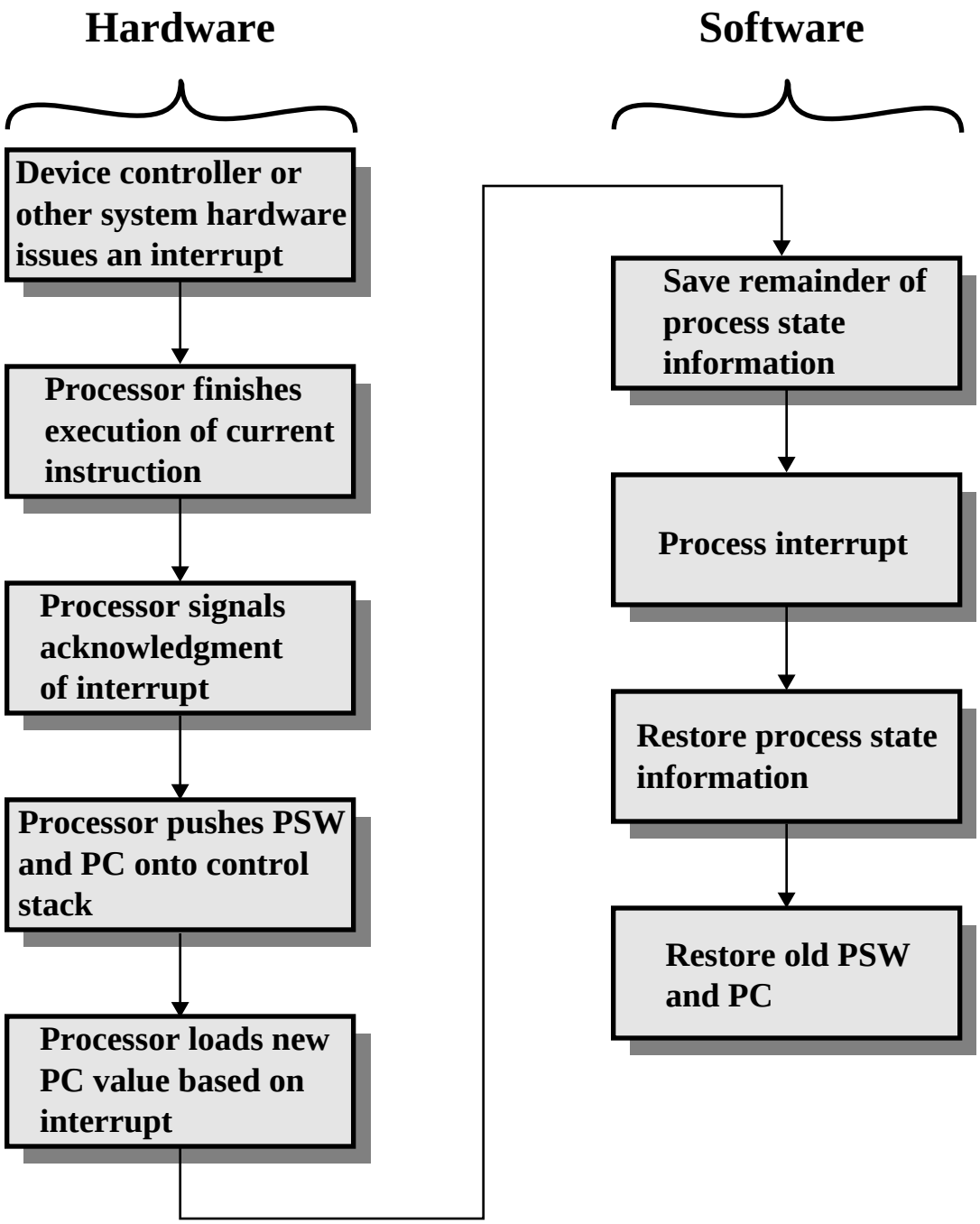

Figure 1.10 Simple Interrupt Processing