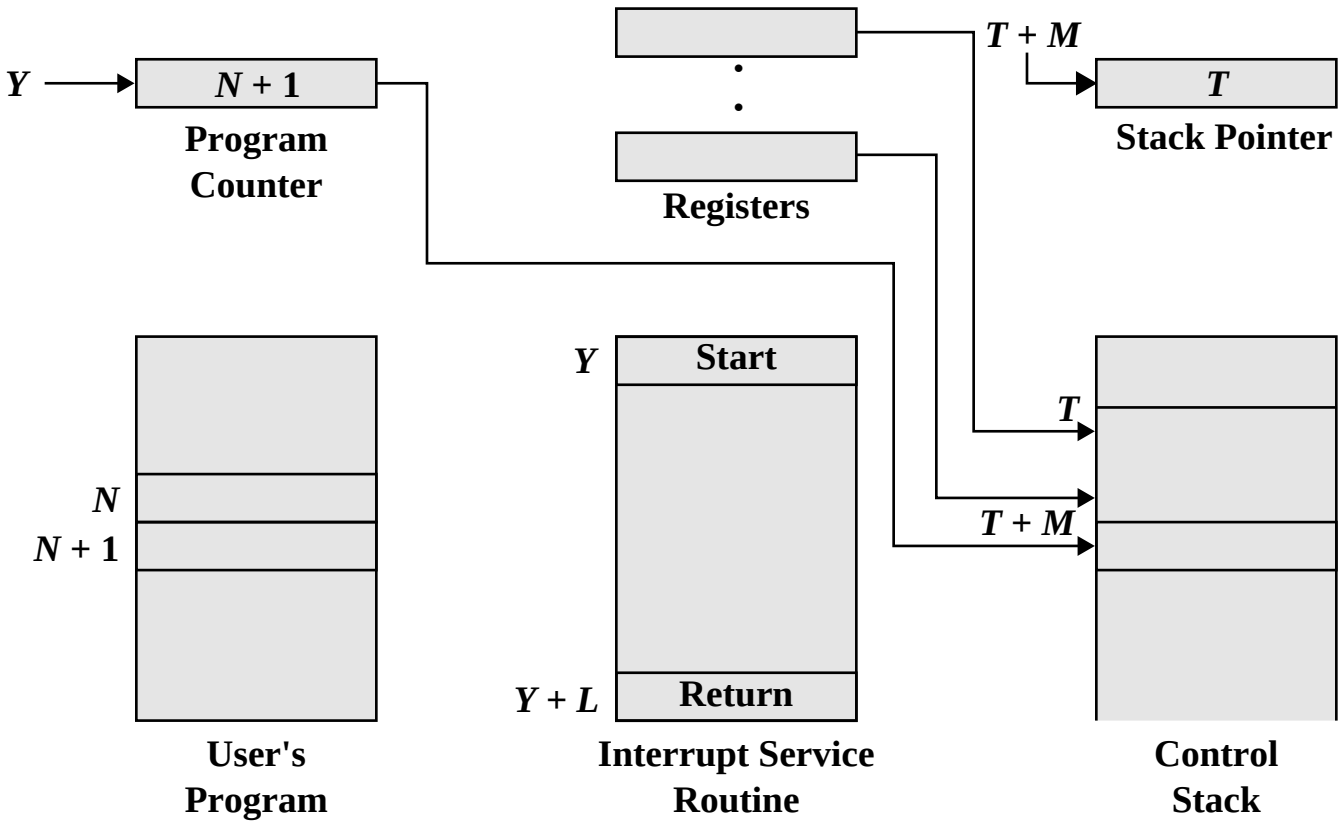
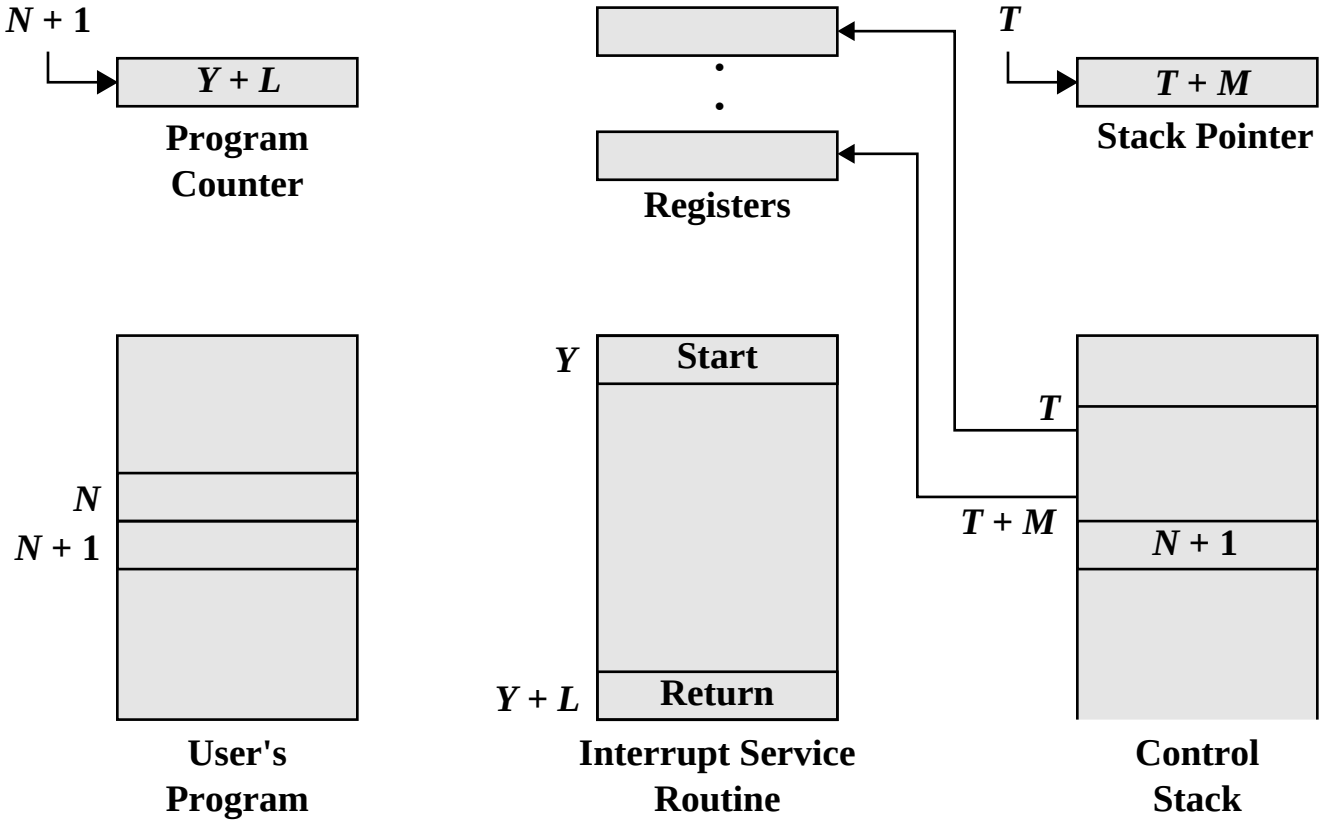


(a) Interrupt occurs after instruction at location *N*



(b) Return from interrupt

Figure 1.11 Changes in Memory and Registers for an Interrupt